## **Amendments to the Drawings:**

Three replacement sheets with corrected Figures 1-3 replace the original sheets for Figures 1-3.

Attachment: Three Replacement Sheets for Figures 1-3

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**REMARKS** 

Claims 1-14 were previously pending in this patent application. Claims 1-

14 stand rejected. Herein, Claims 1, 5, and 10 have been amended.

Accordingly, after this Amendment and Response, Claims 1-14 remain pending

in this patent application. Further examination and reconsideration in view of the

claims, remarks, and arguments set forth below is respectfully requested.

**DRAWINGS** 

Figures 1-3 are objected to. Herein, three replacement sheets with

corrected Figures 1-3 are being submitted to replace the original sheets for

Figures 1-3. No new matter was added. Withdrawal of the objection to Figures

1-3 is respectfully requested.

35 U.S.C. Section 102(b) Rejections

Claims 1-3, 5-7, and 10-12 stand rejected under 35 U.S.C. 102(b) as

being anticipated by Larsen, U.S. Patent No. 5,115,500 (hereafter Larsen).

These rejections are respectfully traversed.

Independent Claim 1 recites (as amended):

A method of processing an instruction, said method comprising:

fetching said instruction using a corresponding address from a memory unit, wherein a plurality of possible meanings are associated

with said instruction;

concatenating a portion of said corresponding address to said

instruction to form an extended instruction, wherein said portion of

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said corresponding address is independent of region of said memory unit from which said instruction is fetched; and executing said extended instruction, wherein said portion of said corresponding address determines a meaning for said extended instruction from said possible meanings. (emphasis added)

It is respectfully asserted that Larsen does not disclose the present invention as recited in Independent Claim 1. In particular, Independent Claim 1 recites the limitation, "concatenating a portion of said corresponding address to said instruction to form an extended instruction, wherein said portion of said corresponding address is independent of region of said memory unit from which said instruction is fetched," (emphasis added). In contrast, Larsen discloses an instruction store (2) with partitioning for accommodating two types of different, incompatible format machine language instructions. [Larsen; Figure 2; Col. 5, lines 34-51]. That is, the portion of the address concatenated with the instruction is dependent on the region of instruction store (2) from which the instruction is fetched. Moreover, if 1/8 of the instruction store total capacity is reserved for type 1 instructions and 7/8 of the instruction store total capacity is reserved for type 2 instructions, the high order three bits of the address must be concatenated with the instruction to know whether the instruction is fetched from a region storing type 1 instructions or from a region storing type 2 instructions. However, Larsen fails to show concatenating a portion of the corresponding address to the instruction to form an extended instruction, wherein the portion of the corresponding address is independent of region of the memory unit from which the instruction is fetched, as in the invention of Independent Claim 1. Therefore, it is respectfully submitted that Independent Claim 1 is not anticipated by Larsen and is in condition for allowance.

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Dependent Claims 2-3 are dependent on allowable Independent Claim 1, which is allowable over Larsen. Hence, it is respectfully submitted that Dependent Claims 2-3 are patentable over Larsen for the reasons discussed above.

With respect to Independent Claims 5 and 10, it is respectfully submitted that Independent Claims 5 and 10 recite similar limitations as in Independent Claim 1. In particular, Independent Claim 5 recites, "...concatenating said portion of said particular address to said instruction, wherein said portion of said particular address is independent of region of said memory unit from which said instruction is fetched," (emphasis added). Moreover, Independent Claim 10 recites, "...operable to concatenate a portion of said corresponding address to said particular instruction to determine a meaning for said particular instruction from said possible meanings before executing said particular instruction, wherein said portion of said corresponding address is independent of region of said memory unit from which said instruction is fetched," (emphasis added). As discussed above, Larsen fails to disclose the cited limitations of the inventions of Independent Claims 5 and 10. Therefore, Independent Claims 5 and 10 are not anticipated by Larsen and are in condition for allowance for reasons discussed in connection with Independent Claim 1.

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Dependent Claims 6-7 and Dependent Claims 11-12 are dependent on allowable Independent Claims 5 and 10, respectively, which are allowable over Larsen. Hence, it is respectfully submitted that Dependent Claims 6-7 and 11-12 are patentable over Larsen for the reasons discussed above.

## 35 U.S.C. Section 103(a) Rejections

Claims 4, 8-9, and 13-14 stand rejected under 35 U.S.C. 103(a) as being unpatentable over Larsen, U.S. Patent No. 5,115,500 (hereafter Larsen) and Official Notice. These rejections are respectfully traversed.

Dependent Claims 4, Dependent Claim 8-9, and Dependent Claims 13-14 are dependent on allowable Independent Claims 1, 5, and 10, respectively, which are allowable over Larsen. Moreover, the Official Notice fails to disclose concatenating a portion of the corresponding address to the instruction to form an extended instruction, wherein the portion of the corresponding address is independent of region of the memory unit from which the instruction is fetched, as in the inventions of Independent Claims 1, 5, and 10. Therefore, Independent Claims 1, 5, and 10 are allowable over Larsen and Official Notice for the reasons discussed above. Since Dependent Claims 4, Dependent Claim 8-9, and Dependent Claims 13-14 depend from Independent Claims 1, 5, and 10, respectively, it is respectfully submitted that Dependent Claims 4, 8-9, and 13-14 are patentable over Larsen and Official Notice for the reasons discussed above.

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## **CONCLUSION**

It is respectfully submitted that the above claims, arguments, and remarks overcome all rejections. All remaining claims (Claims 1-14) are neither anticipated nor obvious in view of the cited references. For at least the above-presented reasons, it is respectfully submitted that all remaining claims (Claims 1-14) are in condition for allowance.

The Examiner is urged to contact Applicants' undersigned representative if the Examiner believes such action would expedite resolution of the present Application.

Please charge any additional fees or apply any credits to our PTO deposit account number: 23-0085.

Respectfully submitted,

WAGNER, MURABITO & HAO, LLP

Jose S. Garcia Registration No. 43,628

Two North Market Street, Third Floor San Jose, CA 95113 (408) 938-9060

Appendix: Three replacement sheets